

Fig. 1
Prior Art

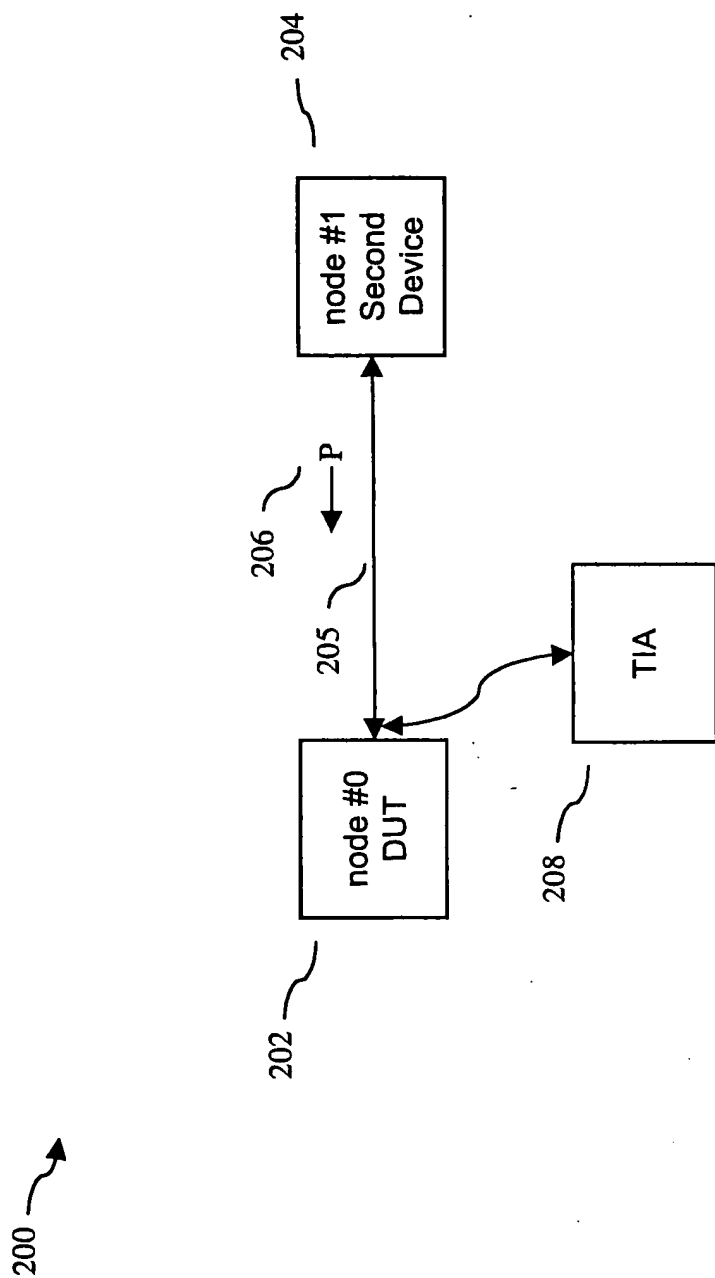


Fig. 2
Present Invention

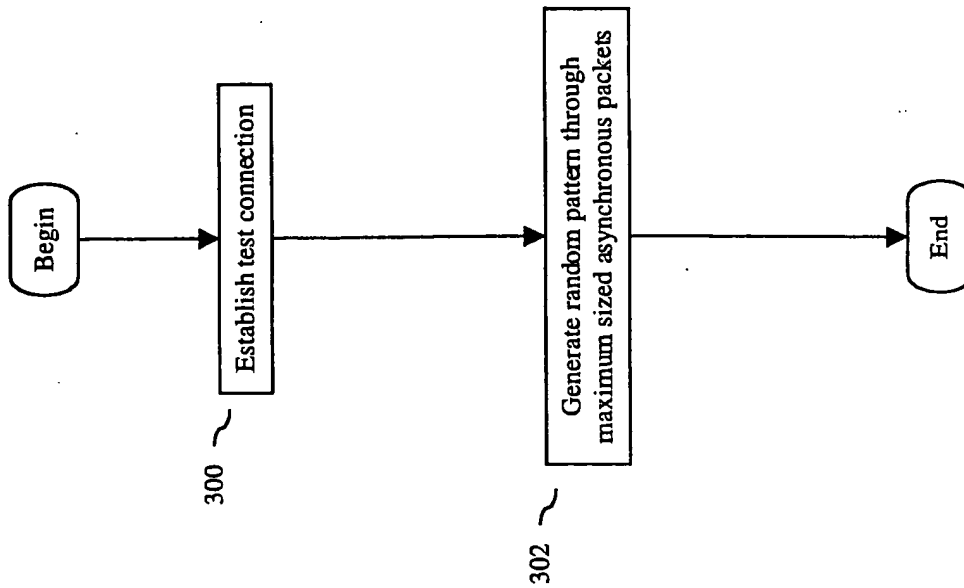


Fig. 3
Random Jitter Test Pattern
According to the Present Invention

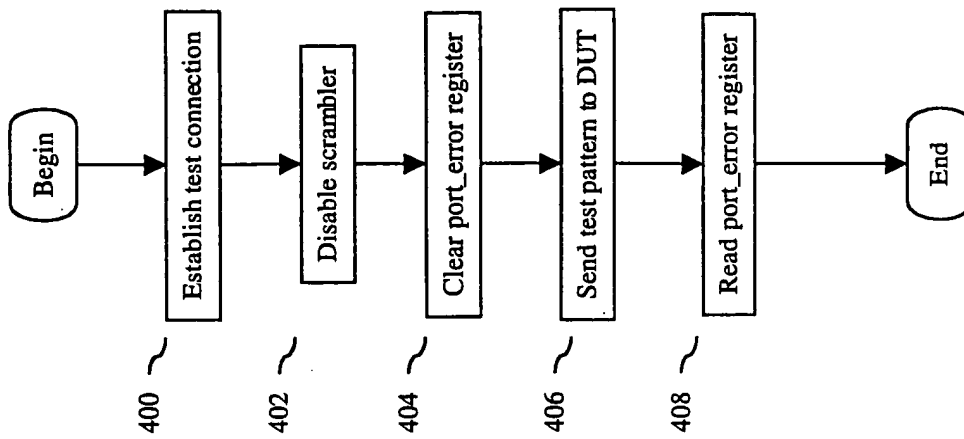


Fig. 4
Jitter Tolerance Pattern
According to the Present Invention

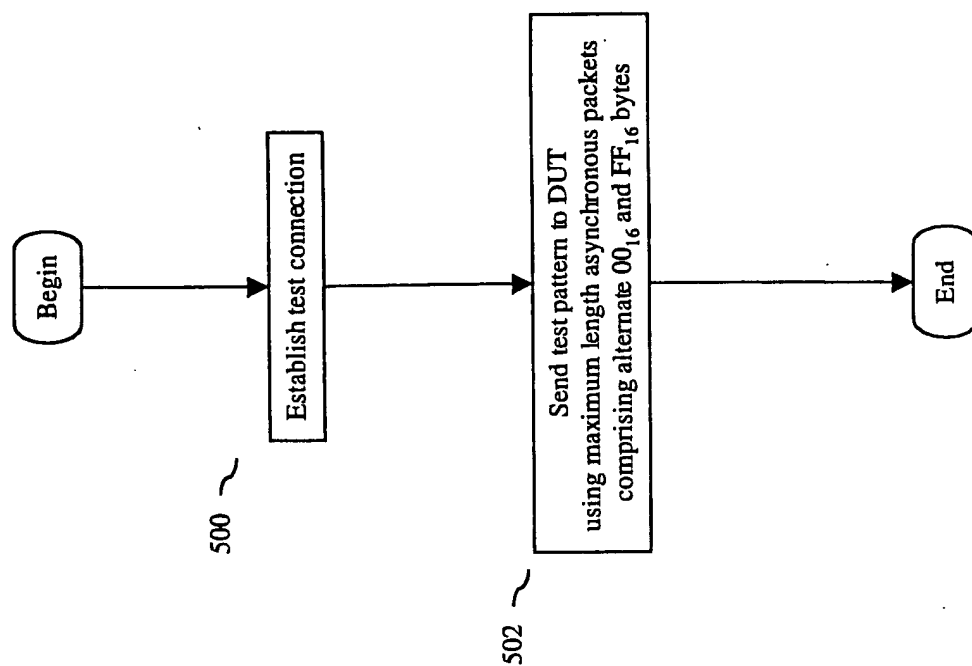


Fig. 5
Supply Noise Pattern
According to the Present Invention